SAULT COLLEGE OF APPLIED ARTS & TECHNOLOGY

SAULT STE. MARIE, ONTARIO

	COURSE OUTLINE
	ELECTRONIC FUNDAMENTALS I
Code No.:	ELN100-6
Program:	ELECTRICAL/ELECTRONICS/COMPUTER ENG. TECH./TECHN'Y
Semester:	ONE
Date:	JUNE, 1984
Author:	W. FILIPOWICH

- ELECTRONIC DEVICES AND CIRCUITS - 3rd Ed.; Boylestad/Nashelsky

New: Revision: X

Date

APPROVED:

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ELECTRONIC FUNDAMENTALS I

Course Name

ELN100-6 Course Number

PHILOSOPHY/GOALS:

This course, along with Electronic Fundamentals II (ELN101), is planned to meet the needs of the technician/technologist who is to work in the electrical/electronic field. It is intended to provide a solid background in fundamentals that is necessary for the study of the more specialized aspects of electronics. The student will become familiar with solid-state devices (diodes and transistors), their characteristics and applications in power supply and basic amplifier circuits. The student will be able to analyze, construct, test and troubleshoot various circuits using theoretical and practical methods.

METHOD OF ASSESSMENT (GRADING METHOD):

- Testing in relation to the theory objectives will make up approximately 60% of the final mark and will consist of at least two major tests plus various short guizzes.
- Testing in relation to the practical (lab) objectives will make up approximately 40% of the final mark and will consist of a formal power supply technical report, lab logbook reports and practical assessments which will include lab attendance, participation, performance, attitude, etc.

TEXTBOOK(S):

ELECTRONIC PRINCIPLES - 3rd Ed.; Malvino (McGraw-Hill)

REFERENCES:

- FUNDAMENTALS OF ELECTRONICS 3rd Ed. Lurch (Wiley)
- GENERAL ELECTRONIC CIRCUITS 2nd Ed.; DeFrance (Holt-Rinehart)
- FUNDAMENTALS OF ELECTRONIC DEVICES 2nd Ed.; Tocci (Merrill)
- ELECTRONIC DEVICES AND CIRCUITS 3rd Ed.; Boylestad/Nashelsky

(Prentice-Hall)

COURSE OBJECTIVES:

At the successful completion of this course, students should meet these General Objectives:

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- Students should have obtained sufficient atomic theory to cope with the understanding of electronic devices and the fundamentals of electrical behavior.
- Students should be familiar with the theory of operation of the following devices: -p-n diode, zener diode, junction transistor.
- Students should be able to do DC analysis of circuits using devices in 2.
- 4. Students should be able to test devices in 2.
- 5. Students should be able to analyze DC power supply circuits.
- 6. Students should be able to analyze DC transistor amplifier circuits.
- Students should be able to construct, analyze, test and troubleshoot various power supply circuits and transistor amplifier circuits using various test equipment.

TEXT: Electronic Principles, 3rd Ed., Malvino

PERIOD TOPIC DESCRIPTION REFERENCE BLOCK T-L Semiconductor Diodes Chapters 1, 2 I 8 - 6 - Introduction to Current Flow - Review of Basic Theorems - Semiconductor Theory - Energy Levels - Doping - PN Diode Formation - Diode Biasing Methods - Diode Circuit Analysis - approximate method - load lines II DC Power Supplies. Chapters 3, 4 - Block Diagram - Sine Wave Analysis - Power Transformers - Rectifier Circuits and Characteristics - Filter Networks - Diode Ratings - Voltage Multipliers - Zener Diode Characteristics - Zener Voltage Regulator 26 - 24 Transistor (BJT) Amplifier Chapters 5, 6, III 7, 8, 9 - NPN/PNP Transistor Characteristics - Regions of Operation - Transistor Blasing Methods Transistor Amplifer Configurations and DC Circuit Analysis - Transistor Ratings and Specifications - CE Amplifier AC Analysis - Amplifier Troubleshooting - Cascaded Amplifiers - Input and Output Impedance - Amplifier Voltage Gain - CC and CB Amplifier Analysis

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